Inventor: H. Montgomery Manning

Title: Integrated Circuitry and a Semiconductor Processing Method of Forming a

Series of Conductive Lines

Assignee: Micron Technology, Inc.

Serial No. 10/648,886, Filed August 26, 2003

Docket No. MI22-2374

INFORMATION DISCLOSURE STATEMENT

PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a continuation application of co-pending application Serial No. 09/526,797, filed April 22, 1997. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

This Supplemental Information Disclosure Statement is being filed together with the filing of the Request for Continued Examination (RCE) Application and before receipt of the first Office Action. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the

fee specified under 37 C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Citation of these references is respectfully requested.

Respectfully submitted,

Reg. No. 44,854

Dated: ____

By

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Sheet 1 of 1

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						FILING DATE August 26, 2003			GROUP 2826		
					U.S. PATENT DOCUMENTS						
*Examiner Initial		Document Number		Date	Name		Class	Subclass	bolass Filing Data If Appropriate		
	AA	4,984	4,055	01/1991	Okumura et al				ļ		
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